

## CPU - SL questions [25]

1. State the four stages of the machine instruction cycle. [4]
  
2. Outline the function(s) of the:
  - (a) The control unit [2]
  - (b) ALU [2]
  - (c) The DATA bus [2]
  - (d) The ADDRESS bus [1]
  - (e) The control bus [1]
  - (f) The clock [1]
  
3. What are the following registers and what are they used for.
  - (a) the MDR [2]
  - (b) the MAR [2]
  
4. Draw a diagram of the CPU showing the components in question 2. [5]
  
5. Explain the role of cache memory in speeding up processing [3]